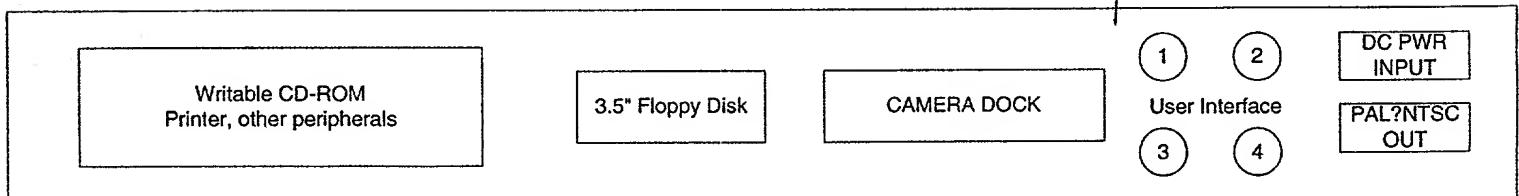
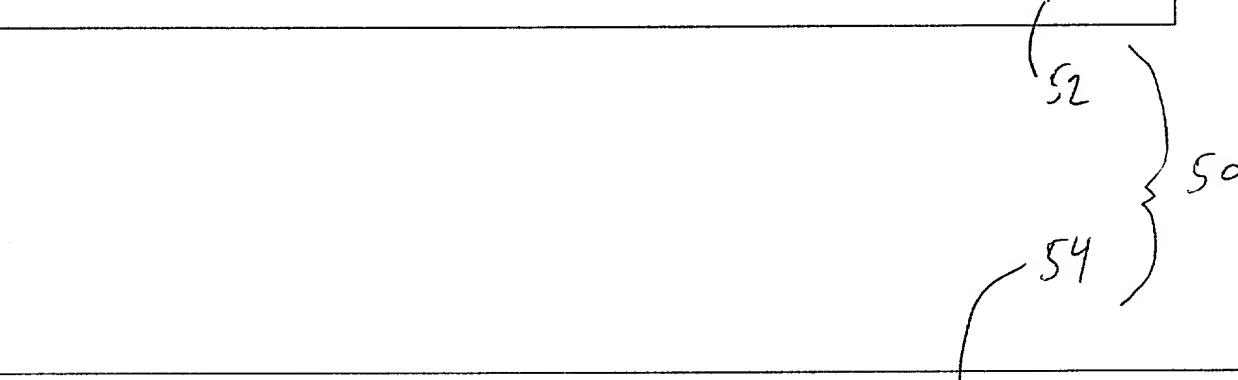


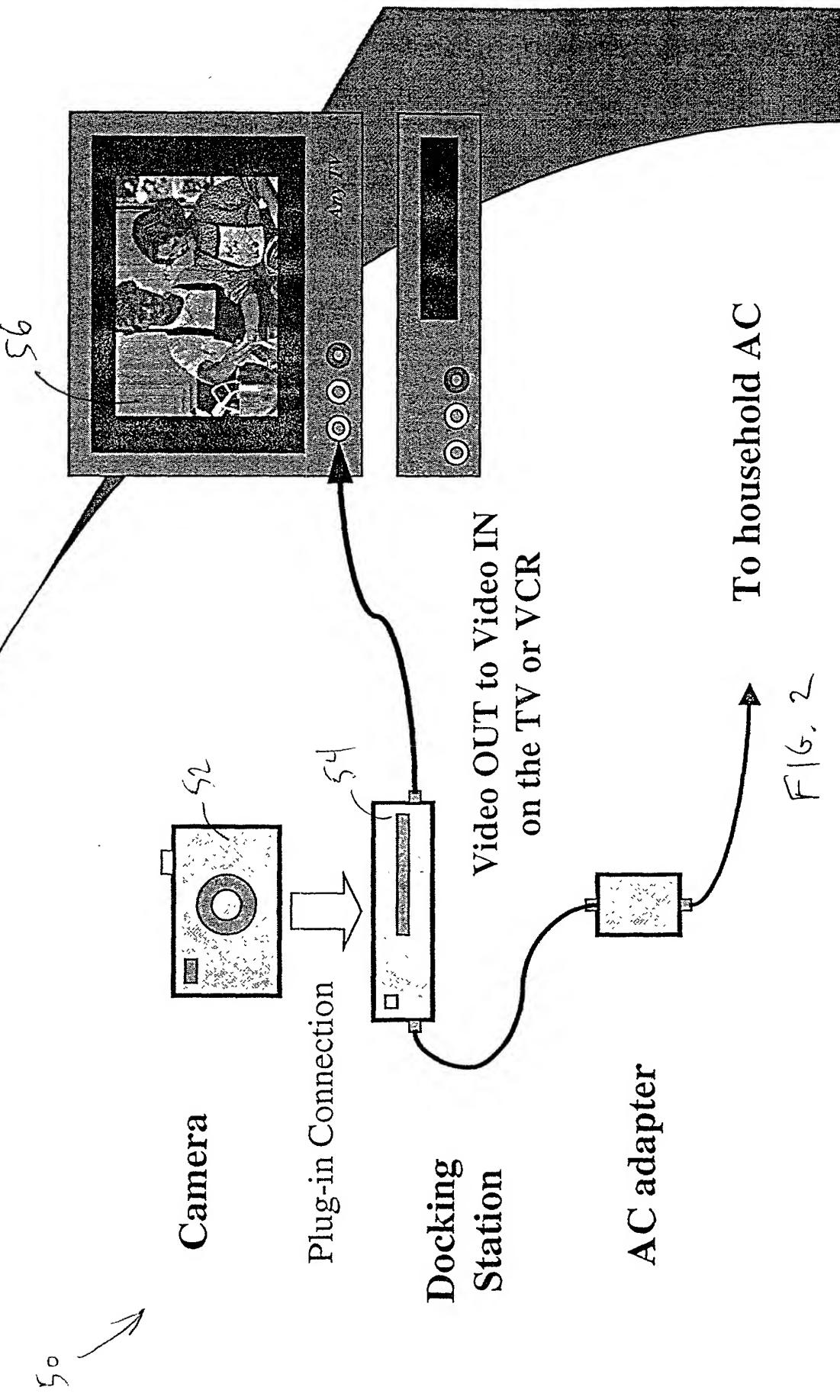
SVI VGA Camera



SVI Docking Station

FIG. 1

© 1998 by the McGraw-Hill Companies



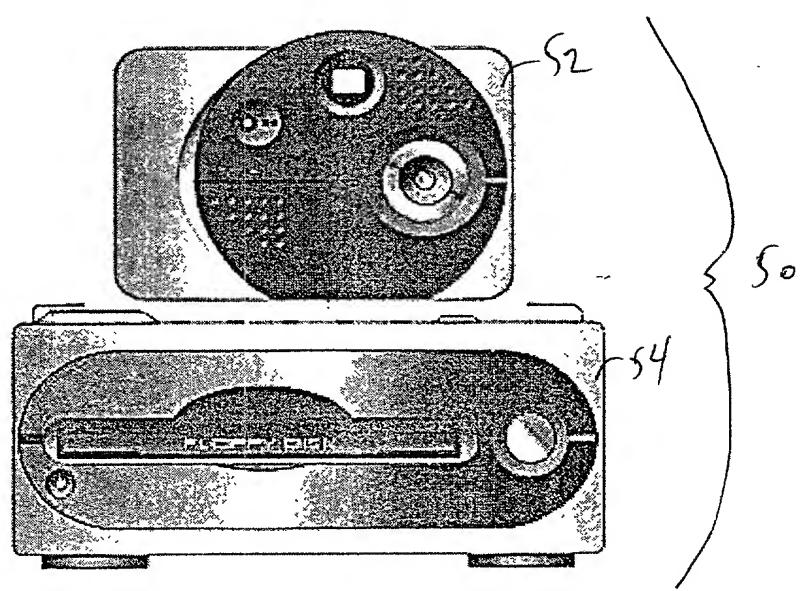
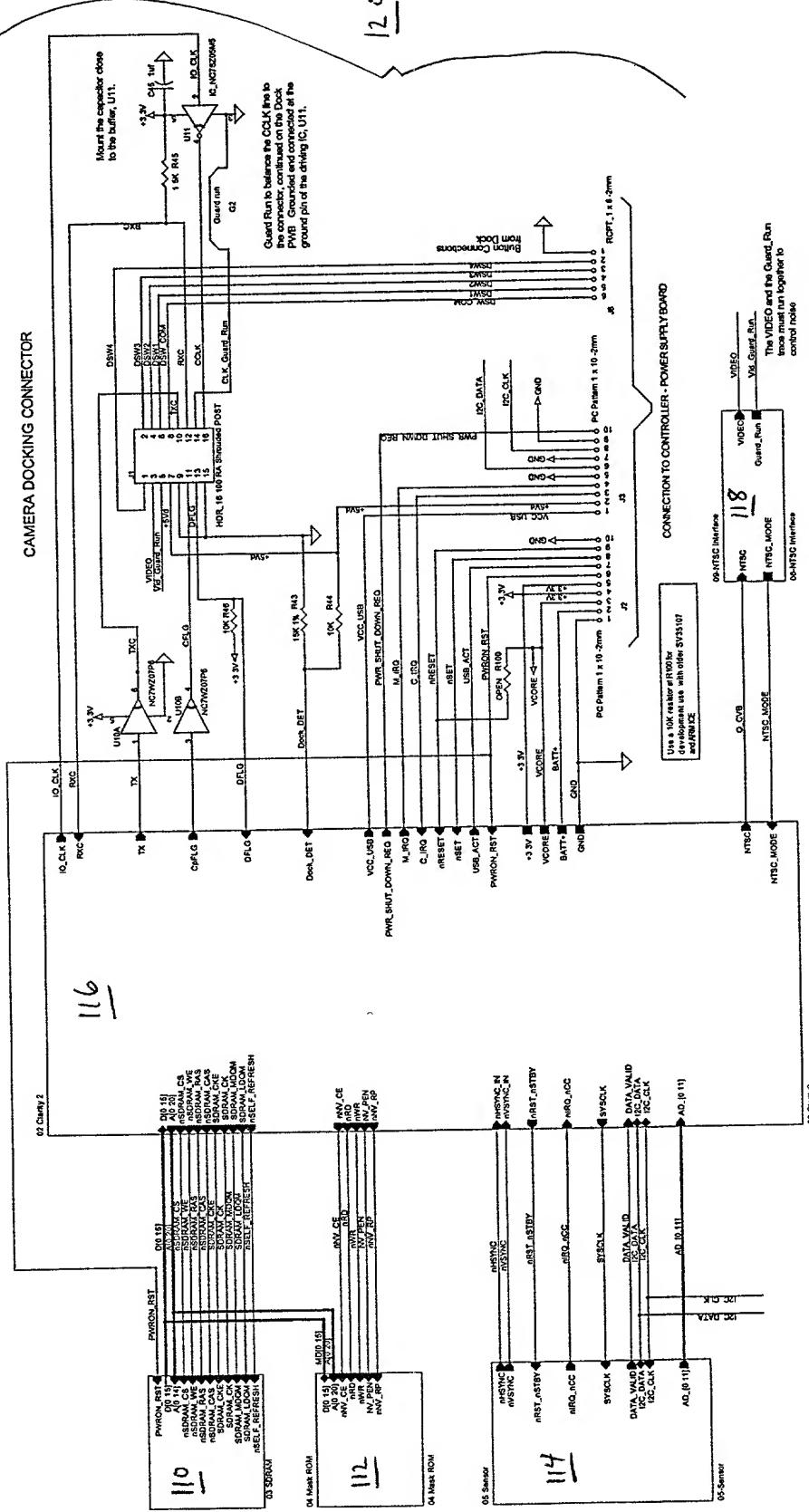


FIG. 3

F16 - 4



NOTE: TOND is a portion of the ground plane protected with cute. It is not a separate function.

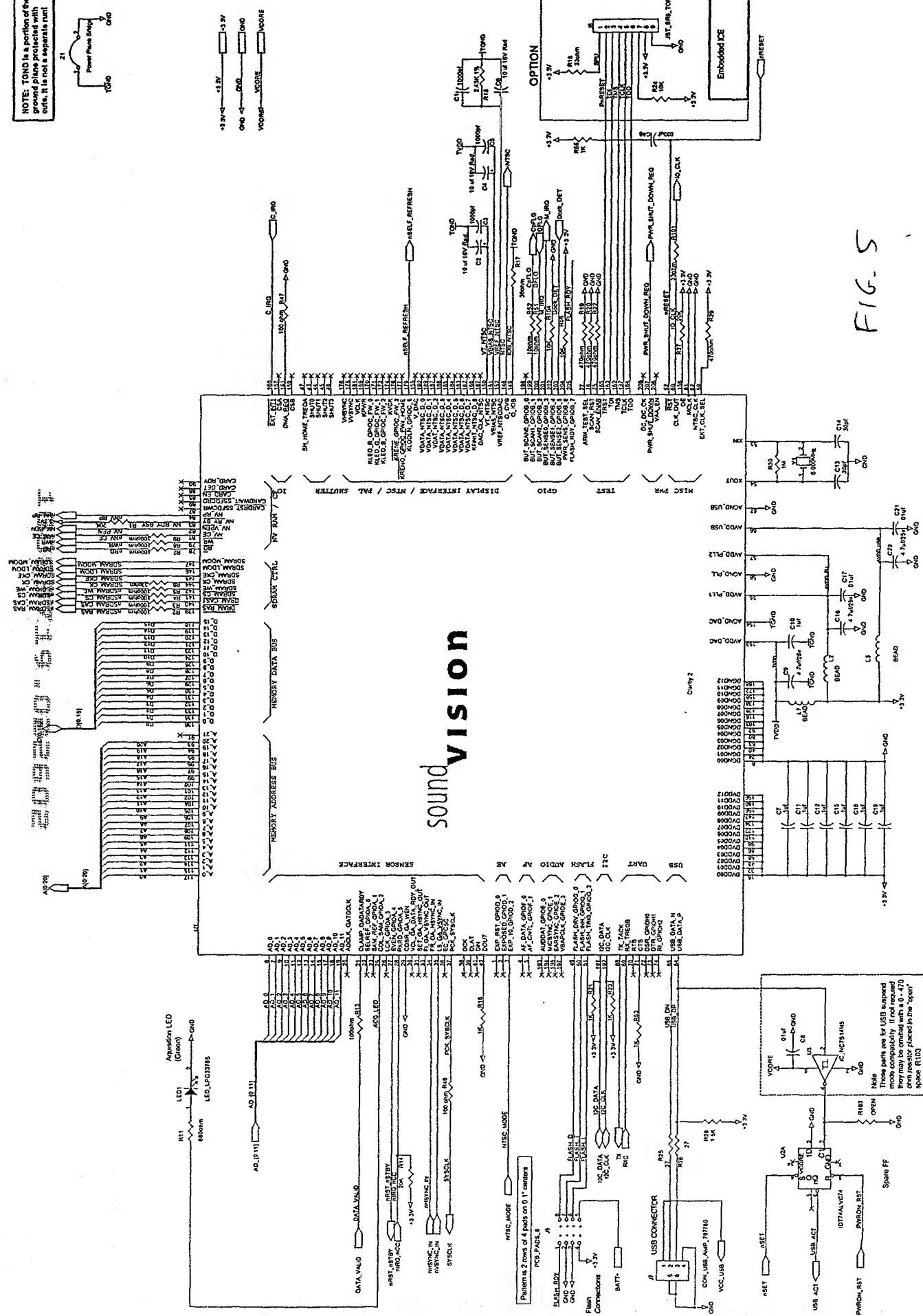
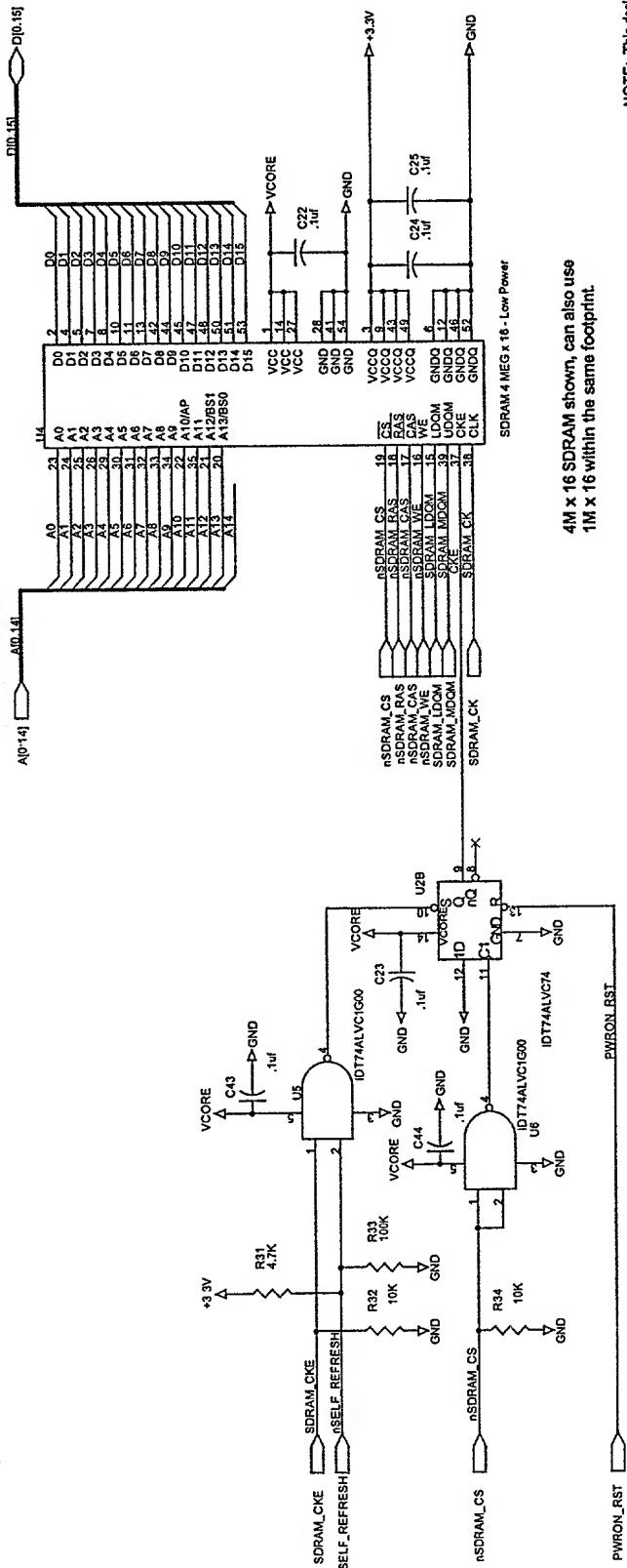


FIG. 5



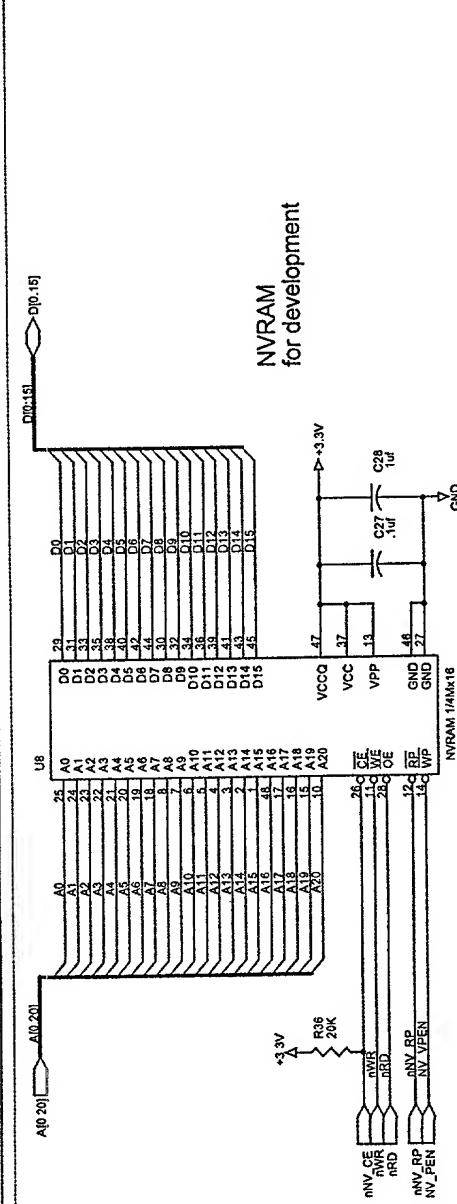
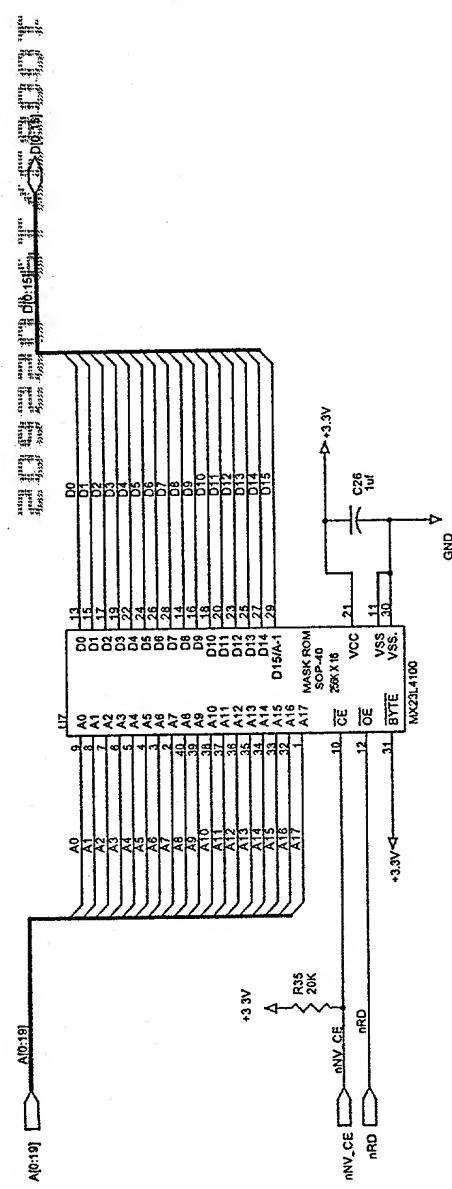
NOTE: This design requires SDRAM parts with isolated Vcc and VccQ on chip.

***** NOTE: *****
Low power SDRAM should be used. Power consumption when the camera is shut down depends on the SDRAM consumption in Self Refresh Mode as the major component of quiescent consumption.

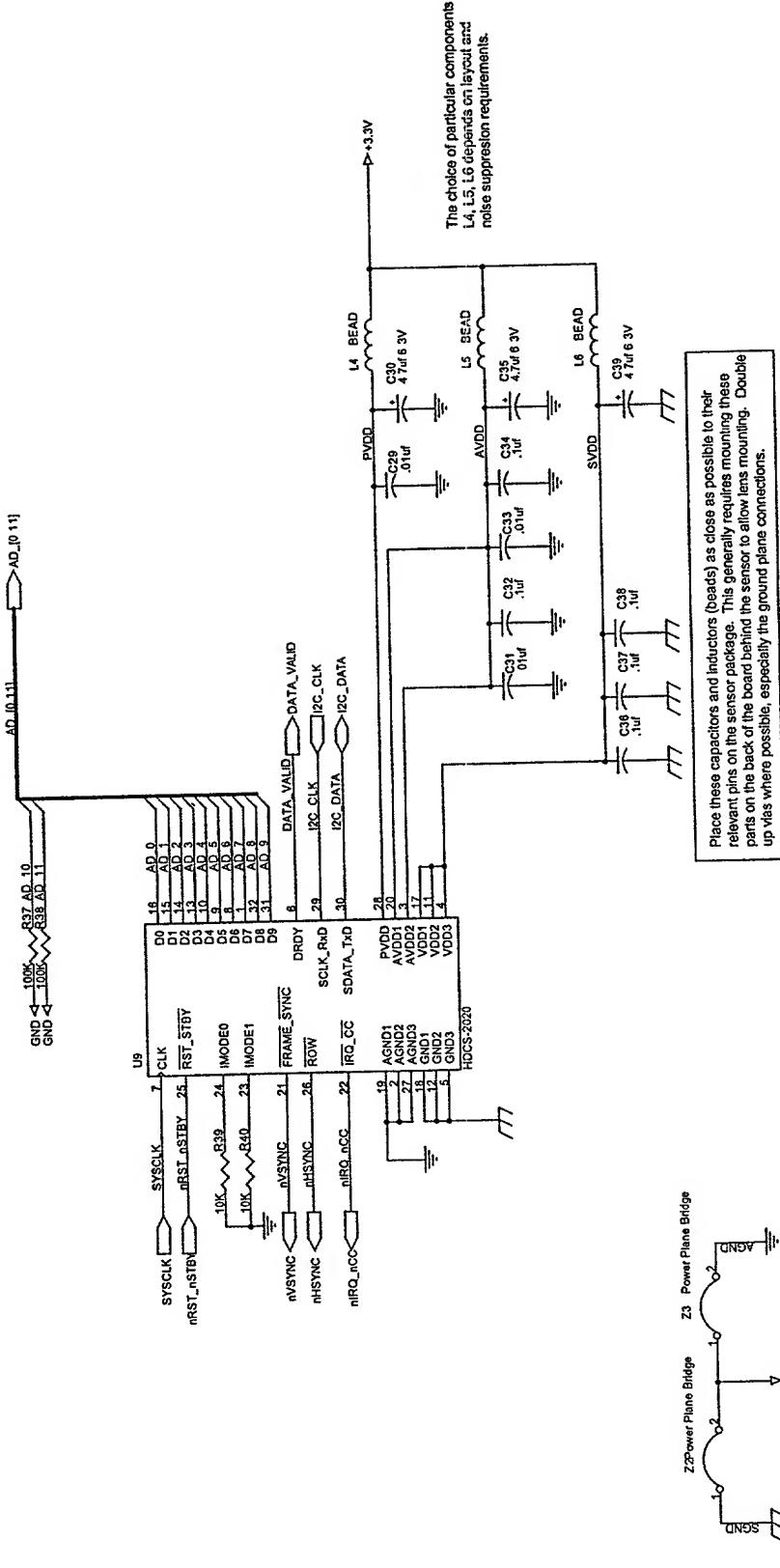
4M x 16 SDRAM shown, can also use
1M x 16 within the same footprint.

Fig. 6

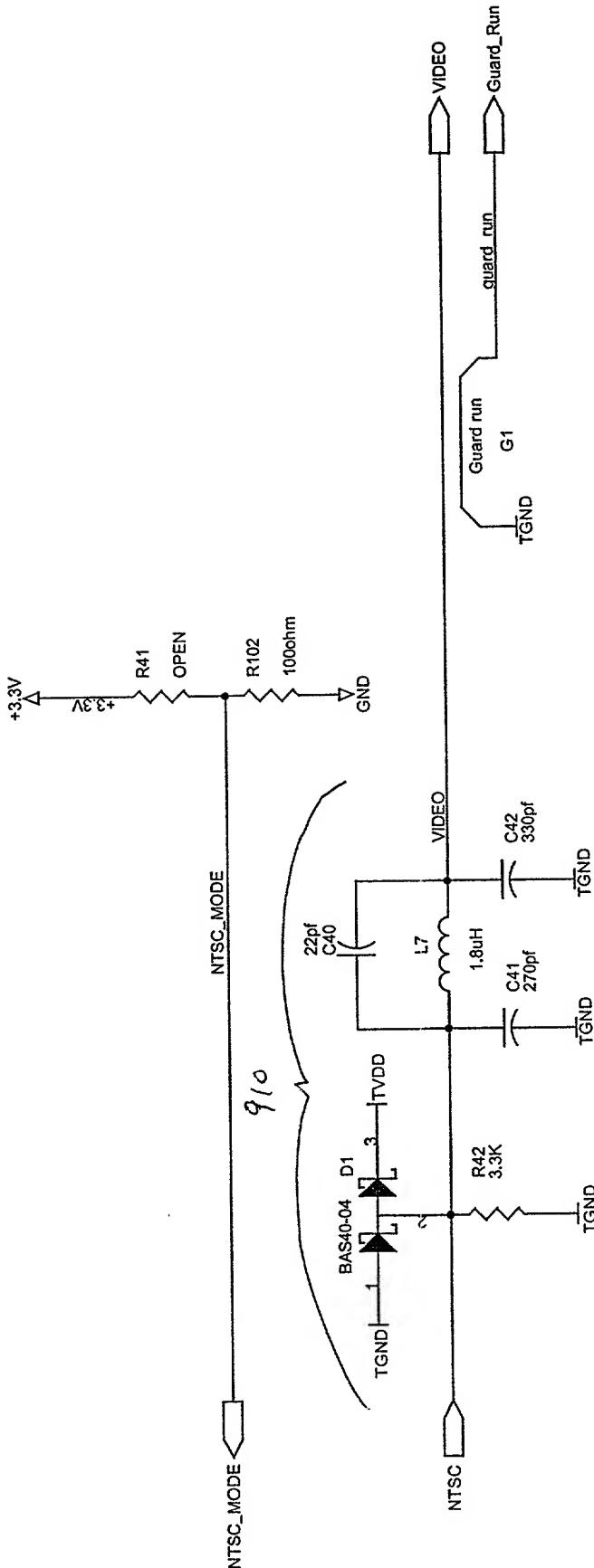
- NOTES:
 1. Address lines on NVRAM limited to match 160 pin QFP-40.
 2. Flash NVRAM is for prototype use only.
 3. Mask ROM pinout matches similar ORI NSM624032E-SOP-40.
 4. Do not duplicate the 20K resistor



F16-7



F16. 8



NOTE:
The diode pin numbering shown here is
non-standard. Make sure the PCB layout
matches the diode(s) used in your design.

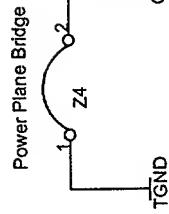
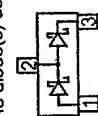
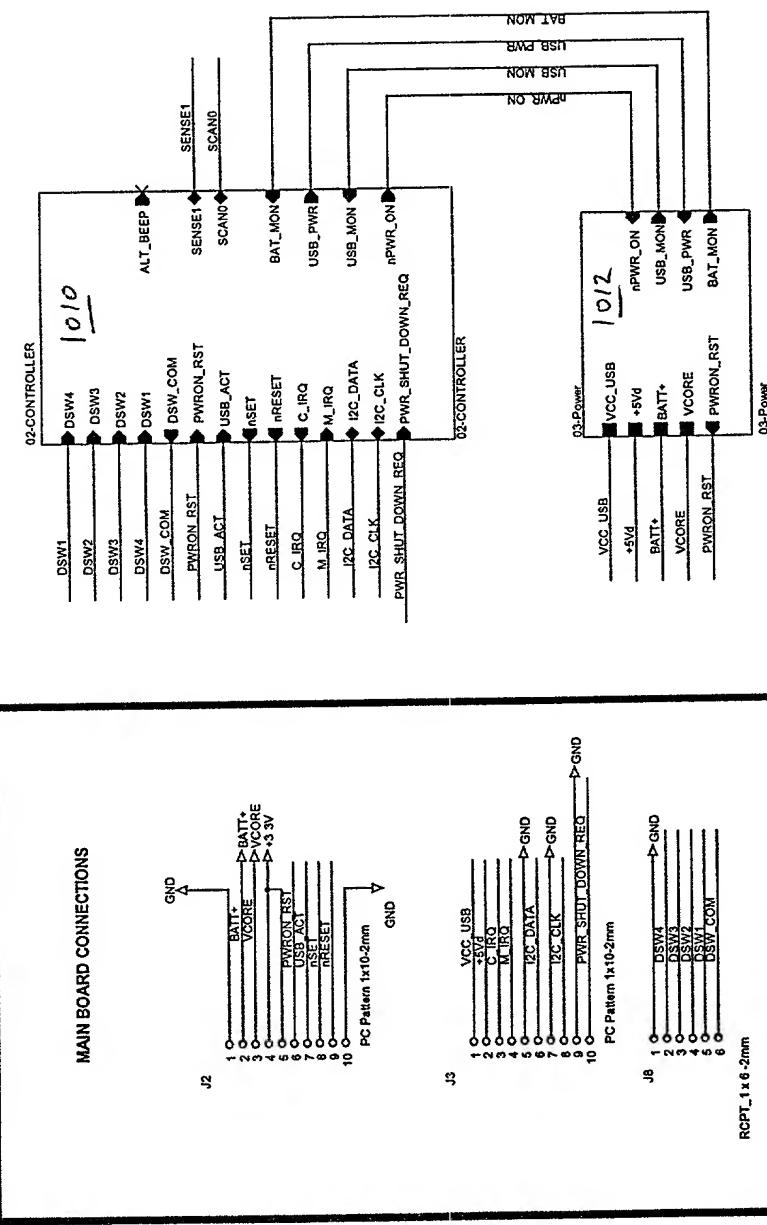
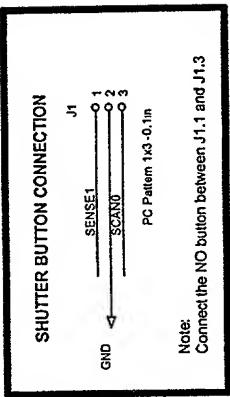


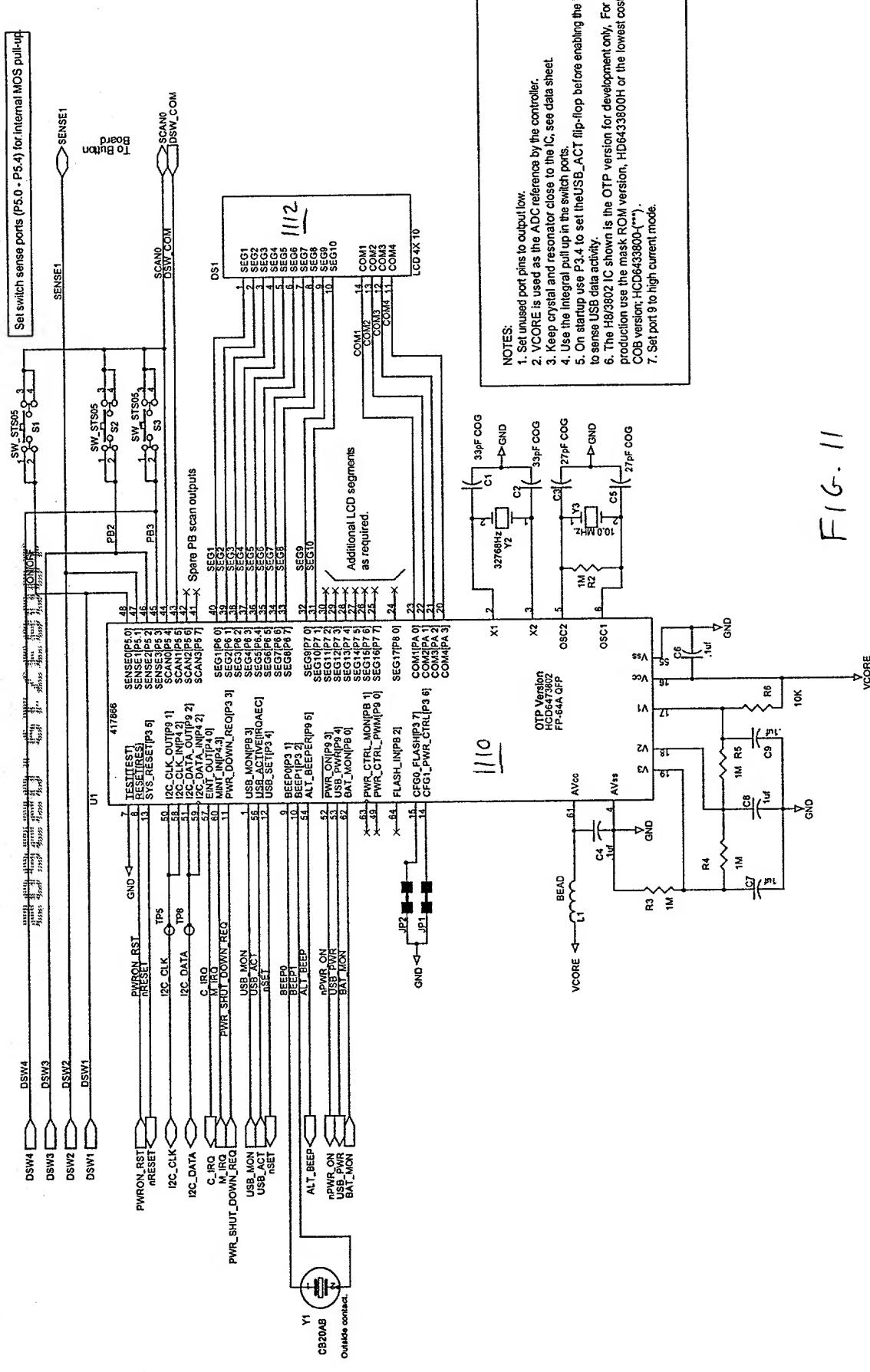
FIG. 9

TGND is a portion of the ground plane
protected with cuts. See Clarity sheet.
It is not a separate run!

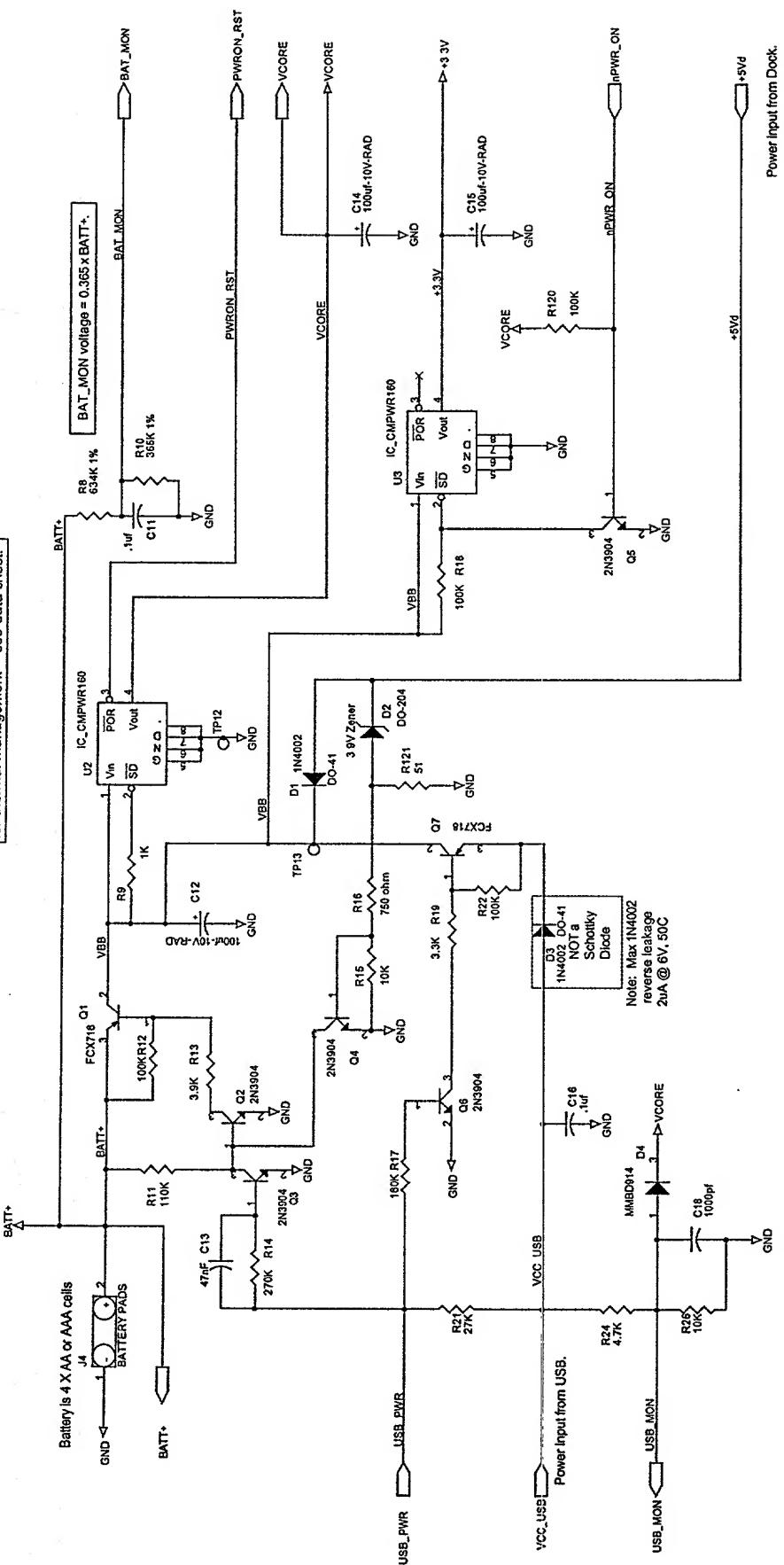


NOTE:
USE VERY WIDE TRACES FOR BAT+, +5Vd,
VCORE, VBB and the +3.3V POWER PATH,
PREFERABLY ON A POWER PLANE

F16.10

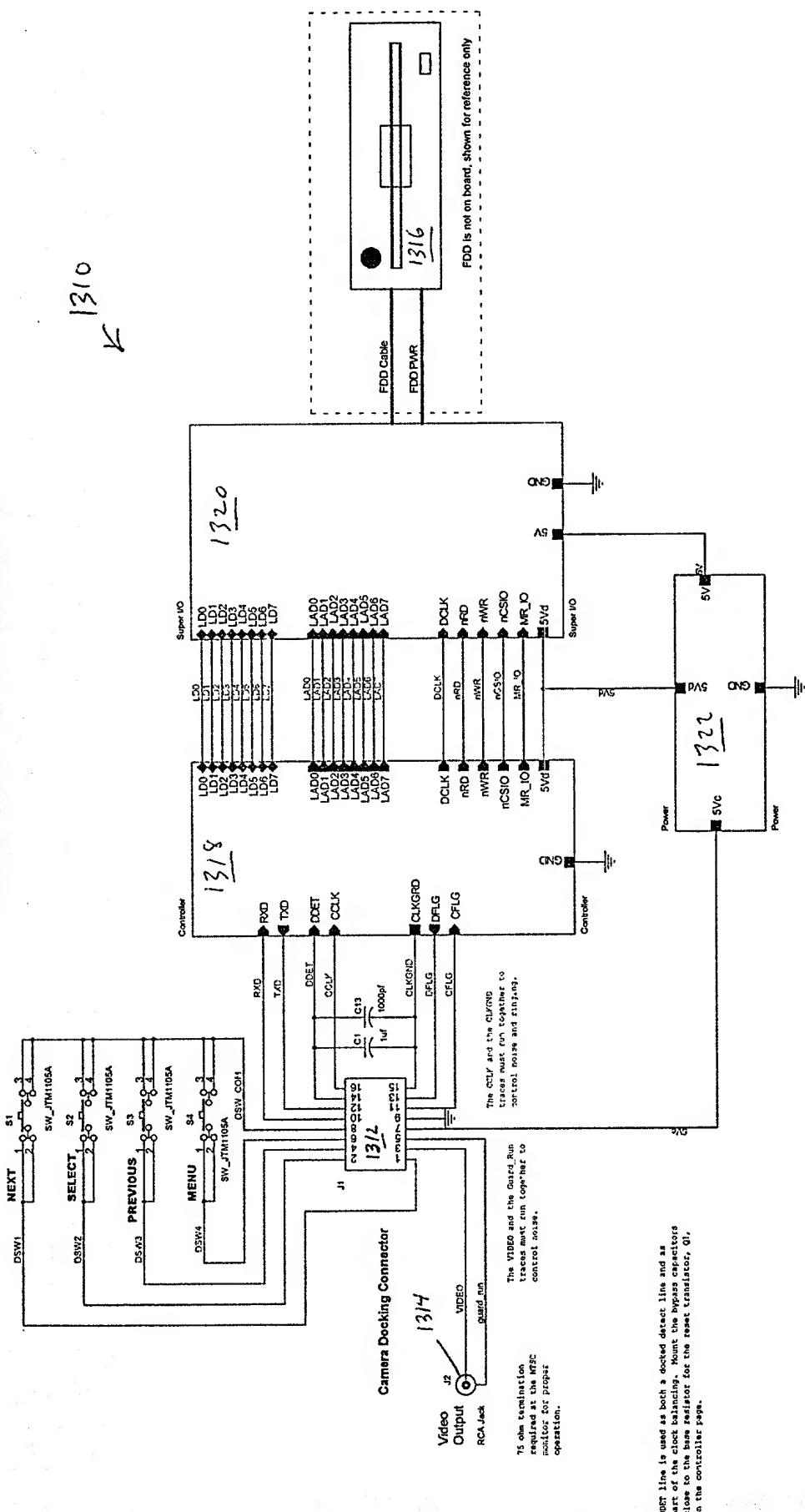


The regulator devices require copper area for thermal management - see data sheet



NOTE:
USE VERY WIDE TRACES FOR BATT+, +5Vd,
VCORE, VBB and the +3.3V POWER PATH,
PREFERABLY ON A POWER PLANE

FIG. 12



E16-13

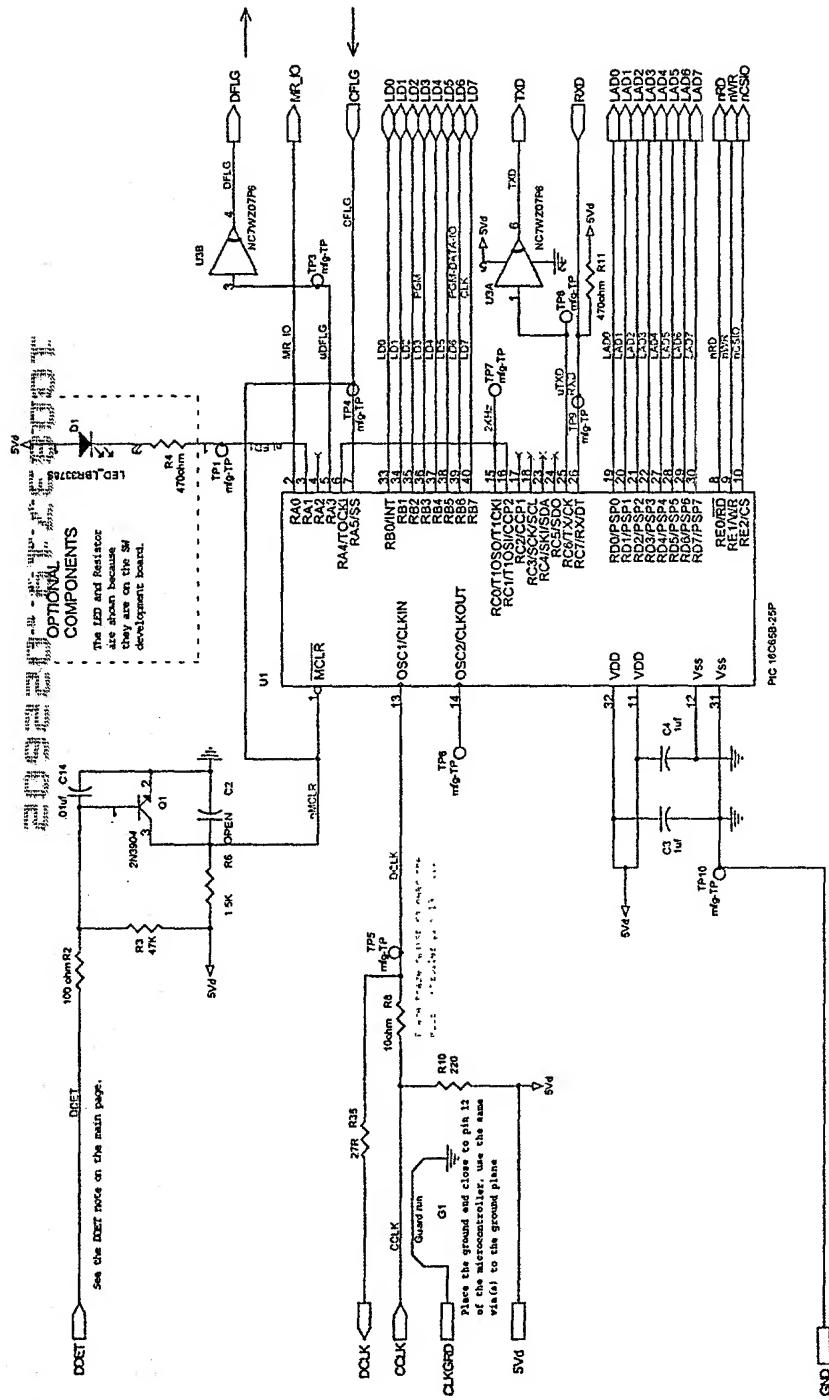
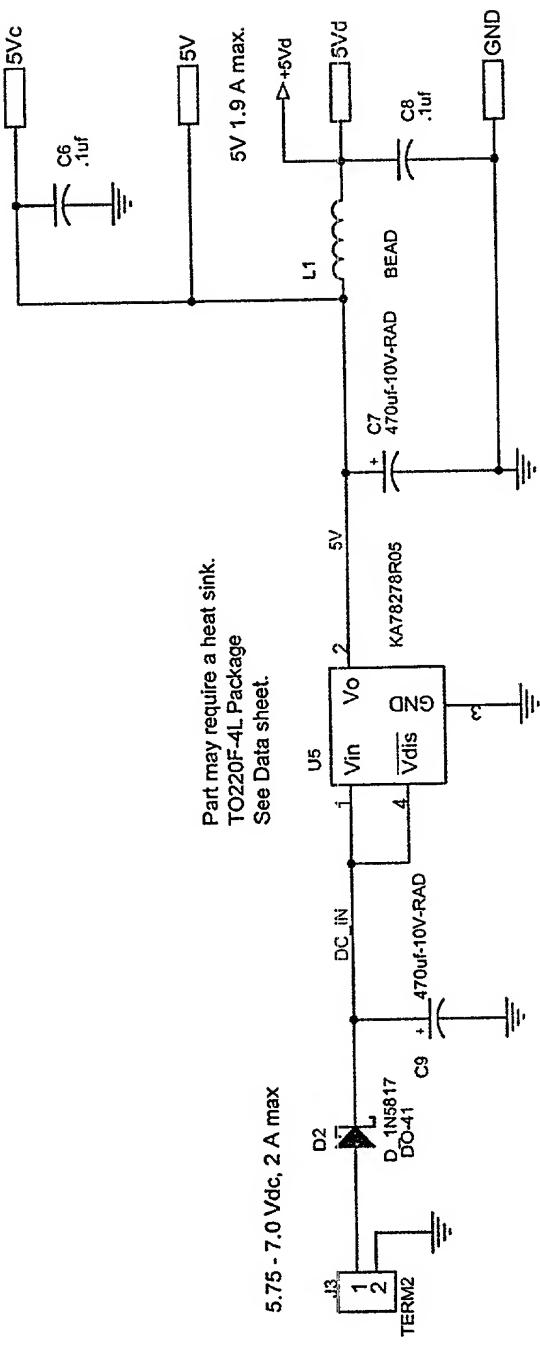


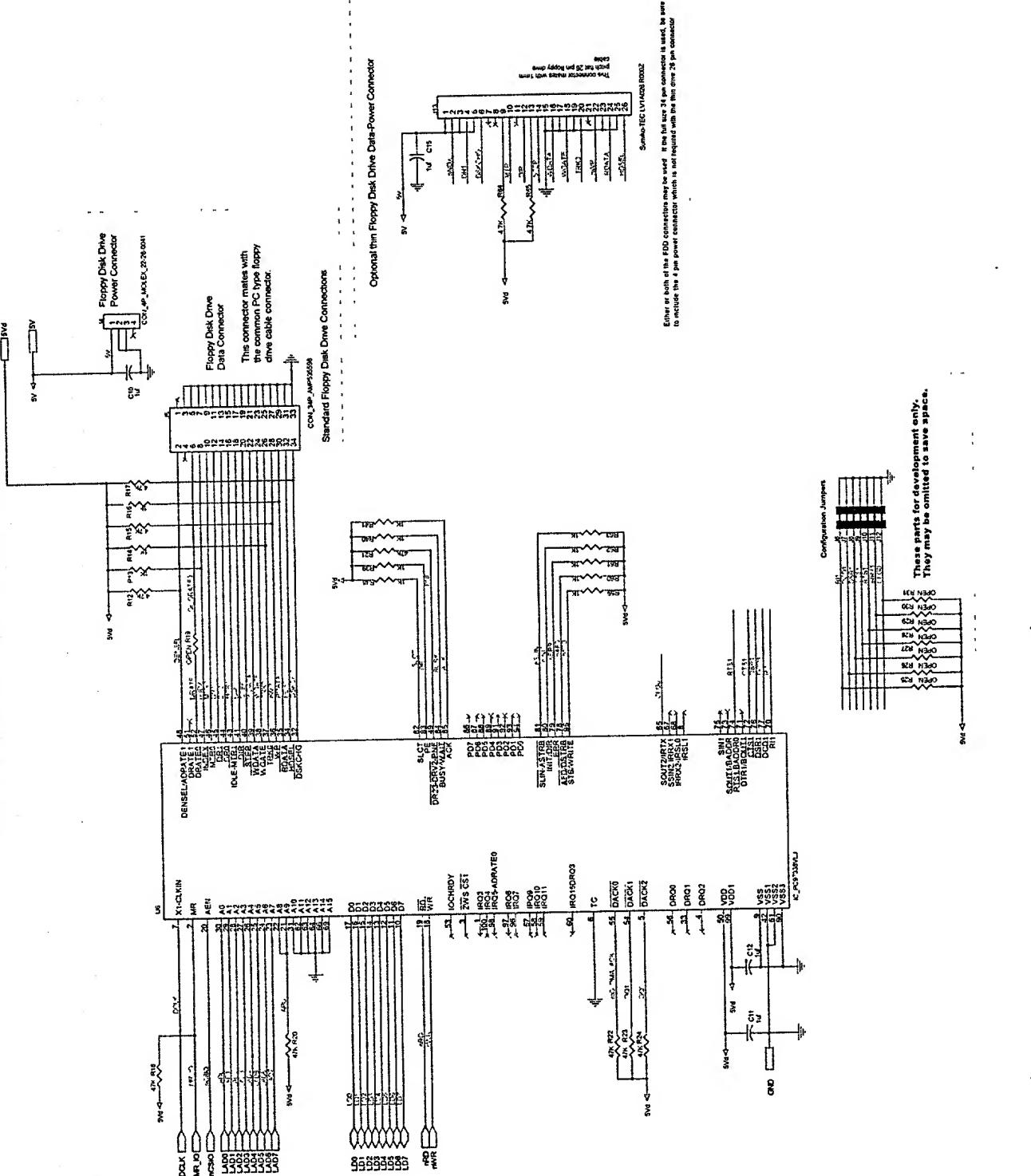
Fig. 14



Note power supply is to be sized for the particular floppy disk
 drive's maximum load, allow ~300 mA for other loads.

Sizing affects choice of U5 and its heatsink, the filter capacitors,
 C7, C9 and the AC adapter rating.

F16. 15



F16.16